

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
1	US 20030035444 A1	20030220	10	Method for synchronizing a communication system via a	370/503	375/354
2	US 6724846 B1	20040420	11	Simple, high performance, bit-sliced mesochronous synchronizer for a source synchronous link	375/354	375/372
3	US 6665317 B1	20031216	22	Method, system, and computer program product for managing jitter	370/516	370/253; 370/353; 375/371
4	US 6606360 B1	20030812	19	Method and apparatus for receiving data	375/354	
5	US 6327274 B1	20011204	7	Method for estimating relative skew between clocks in packet networks	370/516	375/371
6	US 6262999 B1	20010717	5	Generation of primary rate clocks from correction values derived from the received Synchronous Residual Time	370/516	375/355; 375/371
7	US 5993057 A	19991130	11	Apparatus for detecting and averaging data in a digital data stream	714/814	375/226; 375/326; 375/371; 714/812
8	US 5966387 A	19991012	22	Apparatus and method for correcting jitter in data packets	370/516	375/371
9	US 5905756 A	19990518	9	Byte sequencing protocol for an asynchronous data transmission system	375/222	370/472; 370/474; 370/476; 370/509; 370/522; 375/370
10	US 5883924 A	19990316	15	Method and apparatus for PCR jitter measurement in an MPEG-2 transport stream using sliding window	375/226	348/464; 348/500; 370/468; 370/516; 375/371
11	US 5812619 A	19980922	9	Digital phase lock loop and system for digital clock recovery	375/376	327/159; 375/360; 375/361
12	US 5774497 A	19980630	10	Method and apparatus for PCR jitter measurement in an MPEG-2 transport stream	375/226	348/464; 348/500; 370/468; 370/516; 375/371
13	US 5745272 A	19980428	22	Optical data communication and location apparatus, system and method and transmitters and receivers for use	398/118	375/371; 398/1; 398/137

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1	Zwack, Eduard	US 20030035444
2	Lo, Karen	US 6724846
3	Scott, Mark	US 6665317
4	Dunning, David S. et al.	US 6606360
5	Ravikanth, Rayadurgam	US 6327274
6	Fournier, Rob J.	US 6262999
7	Gulick, Dale E. et al.	US 5993057
8	Cloutier, Leo	US 5966387
9	Lamkin, Allan et al.	US 5905756
10	Siu, Michael Yiu-Kwan et al.	US 5883924
11	Runaldue, Thomas Jefferson	US 5812619
12	Block, Simon A. et al.	US 5774497
13	Shipley, Robert T.	US 5745272

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14	US 5703914 A	19971230	18	Clock recovery circuit employing delay-and-difference circuit and pulse-sequence detection	375/355	375/376
15	US 5682384 A	19971028	21	Apparatus and methods achieving multiparty synchronization for real-time network application	370/394	370/517; 375/371
16	US 5668841 A	19970916	11	Timing recovery for variable bit-rate video on asynchronous transfer mode (ATM) networks	375/371	348/500; 370/516
17	US 5654987 A	19970805	15	Clock recovery circuit with reduced jitter	375/355	375/294; 375/354; 375/376
18	US 5276712 A	19940104	11	Method and apparatus for clock recovery in digital communication	375/360	327/18; 375/376
19	US 5229998 A	19930720	5	Method of reducing the low-frequency component of jitter in a digital data transmission system	370/517	375/371
20	US 5025457 A	19910618	23	Synchronizing continuous bit stream oriented terminals in a communications network	375/354	370/507
21	US 4757452 A	19880712	17	Jitter measurement method and apparatus	702/69	324/76.77 , 370/506; 370/516; 375/226;
22	US 4583054 A	19860415	6	Frequency time standard failure monitor	331/2	331/14; 331/17; 331/49; 331/DIG. 2; 375/357
23	JP 54105905 A	19790820		CLOCK SIGNAL REGENERATING SYSTEM		375/371

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14	Nakamura, Seizo	US 5703914
15	Zarros, Panagiotis N.	US 5682384
16	Haskell, Barin Geoffry et al.	US 5668841
17	Nakamura, Seizo	US 5654987
18	Pearson, Jonathan D.	US 5276712
19	Weisser, Alain	US 5229998
20	Ahmed, Hassan M.	US 5025457
21	Scott, Peter M. et al.	US 4757452
22	Basile, Philip C.	US 4583054
23	YAO, SHIZUO et al.	